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ATTORNEY DOCKET NO.:04329.2306

Box Patent Application Assistant Commissioner for Patents Washington, D.C. 20231

Re:

New U.S. Patent Application

Title: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE THEREOF

Inventors and Addresses:

- 1) Yoshio OZAWA Yokohama-shi, Japan
- 2) Yasumasa SUIZU Tokyo, Japan
- 3) Yoshitaka TSUNASHIMA Yokohama-shi, Japan

Sir:

We enclose the following papers, which are not in the English language, for filing in the United States Patent and Trademark Office in connection with the abovereferenced application in accordance with 37 C.F.R. §1.52(d) and §608.01 of the MPEP, Filing of Non-English Language Applications:

Non-English application - 33 pages, including 1 independent claim and 1.

10/25/2000 DTHOMAS 00000022 060918 Classings total. Sale Ref: 00000059 DA#: 060916 130.00₂CH 01 FC:105

Drawings - 15 sheets of formal drawings, containing 66 figures.

- Information Disclosure Statement and Information Disclosure Citation, 3. PTO 1449 with documents attached.
- Check in the amount of \$690.00 represents the filing fee. 4.



Assistant Commissioner for Patents April 27, 2000 Page 2

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicants claim the right to priority based on Japanese Patent Application Nos. 11-121689, filed on April 28, 1999 and 2000-122018, filed on April 24, 2000.

An English translation of the non-English language papers will be filed in the U.S. Patent and Trademark Office within the required time period.

Please accord this application a serial number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Rv.

Richard V. Burgujian Reg. No. 31,744

RVB/FPD/sci Enclosures